EFFECT OF VARYING TEMPERATURE ON GaAS-MESFET ELECTRICAL PARAMETERS

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Abstract
The present paper introduces a simulation model for the drain-current (I-V) characteristics of a submicron GaAs MESFET. This simulation takes into account different electrical and physical parameters as well as the charge distribution in the device active region. In addition, the simulation includes the doping profile and device parameters effects on the ID-VDS characteristic curves for an ion implanted channel MESFET. The temperature effect on device drain current is evidenced and plotted for different biasing conditions. The GaAs MESFET I-V characteristics versus gate length and temperature variations are measured and plotted and then discussed. The simulation results are compared to that published in literature.

Keywords:
GaAs, MESFET, Simulation, I-V Characteristics, Silvaco, Doping Profile, submicron MESFET

1. INTRODUCTION

MESFET transistors are devices intended for high frequency and (or) high power applications. One of these MESFETs is the Schottky gate GaAs MESFET which is the basis for fast digital integrated and microwave circuits operating within the frequency range 30-100GHz. they offer superior performance over the JFET, especially in RF applications as an amplifier. MESFET’s devices generally use compound (III-V) semiconductor technologies for their construction such as GaAs, InP or SiC [1]. Such devices can be used in radar systems, telecommunications satellites, data transmissions, mobile telephony, and vehicle electronics [2] [3]. Due to better features such as high breakdown voltage, high saturation speed, wide bandgap energy and high temperature operation and high mobility in GaAs material [4], these parameters acquire a major importance in determining the output characteristics of a MESFET in GaAs. The power MOSFET transistors have been well promoted in recent technological applications, particularly in energy conversion and transportation fields (aeronautics, railway and automobile) [5].

Several research groups have studied the operation of MESFET transistors at temperatures of 300 to 400° [6] [7]. Considerable efforts have been made to improve the power and frequency performances of GaAs-MESFETs. Several significant challenges have impeded progress on the P-type counterparts including lower intrinsic carrier mobility and difficulties in the growth, doping, and fabrication of high-quality gates and ohmic contacts [7].

The dependence of the characteristics of ion implanted MESFET as a function of their electrical parameters such as the substrate doping and impurity concentrations, is necessary to make adjustments and optimization of the device model used for computer simulations [8-9]. The ion-implantation technique and their low cost and low price of production [10] makes this technique effective in creating a Gaussian doping profile in the channel with better performances for GaAs MESFET, in general, there are a large number of techniques for using uniform doping profile techniques, among which ion implantation is effective for improving MESFET performance in GaAs material [11]. The influence and the effect of temperature on the drain current of GaAs MESFET’s has been compared and performed under a variety of bias and temperature conditions using different voltage-width [6]. In this work we developed a simulation work using SILVACO software. The model is applied to a typical GaAs MESFET under static biasing conditions. The simulation takes into account the reduction of the geometrical parameters such as the channel thickness and the grid length as well as the related physical phenomena. The choice of the substrate doping concentration and the temperature variation scheme, are found to have a noticeable impact on the device current-voltage characteristics. Initially, these characteristics are first obtained at 300°K and then obtained for higher and different temperatures.

The steps of the MESFET structure preparation and doping are achieved using the Athena DC simulation of the $I_d=f(V_{ds})$ and $I_d=f(V_{gs})$ characteristics. The above planar MESFET structure is formed by ion implantation. The active channel region of the device is an n-GaAs layer which can be obtained by ion implanting Si into semi-insulating substrate [11]. Initially, the considered substrate is a semi-insulator which is the intrinsic GaAs with a thickness of 0.305μm, and low doping level of $N_D = 1e^{13}$ [1/cm$^3$]. Next, an active layer of thickness $d = 0.145μm$ of n-type GaAs is deposited with a uniform profile of maximum concentration of the $N_D = 1e^{17}$ [1/cm$^3$]. After that a hard nitride mask is used to define the n+ zones of 0.05μm thickness implanted under the source and drain electrodes. Their concentrations are maximum of value $N_{s} = 2.18e^{18}$ [1/cm$^3$] and highly strongly doped with a GaAs implant. Finally, the deposition of the aluminum metallic contacts Drain and Source of 0.15μm length is achieved and the resulting device length to consider is 1.2μm, provided that the gate length is $L_g = 0.3μm$ (eventually variable).

Once the data are introduced to the Atlas software, the function Grid Electrode Gate with the work function of 4.87eV is assigned. The numerical methods used in this simulation are conservative initially. The Gummel Newton Method was chosen and this notation indicates that the decoupled method (Gummel) is used at the beginning of the simulation for each bias point, if the convergence is not obtained it will be switched to the coupled method (Newton) [12]. The advantage of the method is the most robust for initial preload steps and for complex devices. Moreover, it consumes more processor time. Other models have been activated, SRH recombination (Shockley-Read-Hall recombination), the fldmob parameter activates the mobility dependent on the electric field, Conmob specifies the mobility...
dependent on the concentration, the model of Selberherr impact ionization is activated and activated, Temp (temperature dependence), $V_{sat}$ (the parameter saturation velocity). The simulation work is done under few experimental data and considerations as listed in Table 1, the structure that has been considered shown in Fig. 1.

Fig. 1. Typical 2D Structure of GaAs MESFET

2. DEVICE AND PARAMETERS

The basic MESFET device structure used in the present simulation shown in Fig. 1 and its electrical and physical parameters are given in Table 1. The device in Fig. 1 considered in this study. Their parameters are described in Table 1. The main parameters are the gate length and channel doping concentration.

Table 1. Parameters of the Structure for MESFET

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>$L_g$</td>
<td>0.3 $\mu$m</td>
<td>Length Gate</td>
</tr>
<tr>
<td>$N_d$</td>
<td>2.1013 $cm^3$</td>
<td>Channel doping</td>
</tr>
<tr>
<td>$N_p^+$</td>
<td>2.1018 $cm^3$</td>
<td>Drain and Source donors Doping</td>
</tr>
<tr>
<td>$N_a^-$</td>
<td>1.1013 $cm^3$</td>
<td>Acceptors Concentration Substrate</td>
</tr>
<tr>
<td>$A$</td>
<td>0.145 $\mu$m</td>
<td>Channel Thickness</td>
</tr>
<tr>
<td>$L_{gd}$</td>
<td>0.15 $\mu$m</td>
<td>Gate-Drain Electrode Spacing</td>
</tr>
<tr>
<td>$L_{gs}$</td>
<td>0.15 $\mu$m</td>
<td>Gate-Source Electrode Spacing</td>
</tr>
<tr>
<td>$V_{bi}$</td>
<td>0.87 $V$</td>
<td>Potential barrier</td>
</tr>
<tr>
<td>$V_{sat}$</td>
<td>1.2 $e^3 cm/s$</td>
<td>Saturation Velocity</td>
</tr>
</tbody>
</table>

3. NUMERICAL ANALYSIS

In this work, we used the simulator TCAD-SILVACO to study the performances of MESFET GaAs transistors, namely the influence of certain geometric and physical parameters on the characteristics of this device.

The basic equations are: The Poisson equation that relates the electrostatic potential to the space charge density given by Eq. (1).

$$\frac{\partial^2 V}{\partial x^2} = -\frac{q}{\varepsilon_{Gat}} \left( N_{a^-} - N_i + p - n + N_{p^+} - N_{n^-} \right)$$

where, $V$ is the electrostatic potential, $\varepsilon_{Gat}$ is the local permittivity for GaAs, $q$ represent the elementary charge, $N_{a^-}$ and $N_i$ are the donor density and acceptor density respectively, $n$ and $p$ are the concentrations of free electrons and free holes respectively, $N_{a^-}$ and $N_{p^+}$ are the ionized deep acceptors and deep donor’s density respectively.

The current densities of electrons and holes, are given by Eq. (2) and Eq. (3),

$$J_n = q\mu_n n \nabla \phi$$

$$J_p = q\mu_p p \nabla \phi$$

These equations are expressed in terms of quasi-Fermi levels, where, $J_n$ and $J_p$ are the current density of electrons and holes respectively, $\mu_n$, $\mu_p$, $q$ are the mobility of electrons, mobility of holes and charge electron respectively.

The more conventional formulation of current density equations given by Eq.(2) and Eq.(3), can be written taking into account the drift-diffusion (Einstein equation) as well as the effective electric field are given by the two-following relation:

$$J_n = qD_n \nabla + q\mu_n n E_n$$

$$J_p = qD_p \nabla + q\mu_p p E_p$$

where, $D_n$ and $D_p$ represents the diffusion coefficient of electrons and holes respectively $E_n$ and $E_p$ are the effective electric field for electrons and holes respectively, $n$ as electrons concentration and $p$ as hole concentration.

The diffusion coefficient and the effective electric field will be replaced by the flowing quantities (Eq.(6) and Eq.(7))

$$D_{n,p} = \frac{kT}{q} \mu_{n,p}$$

$$E_{n,p} = -\nabla \left( \psi + \frac{kT}{q} \ln(n) \right)$$

where, $E_{n,p}$ and $D_{n,p}$ represents the effective electric field and the drift-diffusion of electrons and holes respectively, $T$ being the lattice temperature, $n_i$ is the effective intrinsic concentration, $\mu_{n,p}$ is the mobility (indices $n$, $p$) of electrons or holes concentration and $k$ is the Boltzmann constant.

3.1 FIELD DEPENDENT MOBILITY MODEL

The field dependent mobility for $n$ and $p$ respectively used by Silvaco are:

$$\mu_n = \frac{\mu_n^L}{\sqrt{1+1.54e^{-3}E}}$$

$$\mu_p = \frac{\mu_p^L}{\sqrt{1+5.35e^{-3}E}}$$

where, $\mu_n^L$ and $\mu_p^L$ is the low field electron and hole mobility respectively, $E$ is the electric field parallel to the current flow, and $D$ is a unit less experimentally determined parameter.

These equations are solved by the method of Newton Gummel, several models used in the simulation, which take into account (Fdmob model), (Connmob model), (impact of Selb.model), (SRH model) and (TEMP model) [12].

- **Fdmob model**: the parallel field depending on the electric mobility.
- **Connmob model**: the concentration as function mobility.
- **Impact of Selb.model**: impact ionization.
• **SRHmodel**: ShockleyRead-Hall recombination.
• **TEMP**: temperature dependence.

### 4. RESULTS AND DISCUSSIONS

#### 4.1 DEVICE DC-COMPARTMENTS

The simulations are performed on the MESFETs and the curves obtained underline the performances of the device.

The IV characteristics = \( f(V_{ds}, V_{gs}) \) for a device which holds variations of the different physical and geometrical parameters of the proposed structure are presented by the figures below. The current \( I_{ds} \) increases with the increase of the density of the charge concentration in the channel. Fig. 2, Fig. 3, and one pays well a saturation difference for two gate lengths used in our simulation, this difference is related to the metal-semiconductor contact interface and the trap density on the space charge area and length gate.

**Fig. 2.** Characteristics of \( I_{ds}-V_{ds} \) of the MESFET in comparison with two gate lengths with different concentrations, obtained at \( V_{gs} = -0.2 \)V, the drain voltage from 0 to 1V with a 0.05V step

**Fig. 3.** Characteristics of \( I_{ds}-V_{ds} \), with variety of values of \( L_{gs} \), the concentration of donors set at \( N_d = 2 \times 10^{17} \) cm\(^{-3} \), \( V_{gs} = -0.2 \)V, the drain voltage from 0 to 1V in steps of 0.05V

**Fig. 4.** Characteristics of the currents \( I_{ds} \) as a function of the voltage \( V_{ds} \) of the MESFET, with different values of \( V_{gs} \), the concentration of the donors \( N_d = 2 \times 10^{17} \) cm\(^{-3} \), \( L_g = 0.3 \mu m \)

The Fig. 4 shows the variations of drain-to-source current (\( I_{ds} \)) as function drain-source voltage (\( V_{ds} \)) for different values of \( V_{gs} \) at room temperature condition. It can be seen that the apparatus indicates a maximum drain current at \( V_{ds} = 1 \)V for \( V_{gs} = 0 \)V is 0.18mA, note that this value connected to the doping of the channel. The origin of these anomalies was attributed to the presence of traps in such devices, increase of the charge density in the (ZCE), therefore the current will be larger with maximum doping.

**Fig. 5.** Characteristics of \( I_{ds}-V_{gs} \) of the MESFET at different values \( L_{gs} \), the drain voltage \( V_{ds} = 0.3 \)V, the Donors concentration fixed \( N_d = 2 \times 10^{17} \) cm\(^{-3} \)
Influence of Substrate and Temperature on GaAs MESFET Electrical Parameters

M. DJOUDER, B.H. Salma and M. Kadi, unpublished research. We notice that the characteristics of a MESFET, in dependence with the temperature, voltage bias \( V_{gs} \), the concentration of the donors \( N_d = 2 \times 10^{17} \text{cm}^{-3} \), and gate length \( L_g = 0.3 \mu \text{m} \) show significant device dependence on the temperature shown in the first figures for \( T = 300 \text{K} \), to compare with Fig. 7 and Fig. 8 is clearly remarkable and visible on device characteristics IV for different temperatures, the temperature changes the forms curves in the linear region as nonlinear just before saturation, we can say that at low temperatures, the device is rapidly saturated at low drain voltages, this is due to electron generation and recombination speeds, electrostatic potential, electric field strength, electron concentrations, electron current density, electron speeds and mobility.

5. CONCLUSIONS

The present contribution achieved a simulation for an eventual implementation of a MESFET device under typical conditions of temperature and biasing. The influence of the physical, geometrical and electrical parameters of the GaAs MESFET transistor is studied using the TCAD-SILVACO simulator that is used in its two-dimension version.

The simulations results of the GaAs MESFET structure are obtained using the Atlas-SILVACO tool. We notice that the temperature is one of the basic parameters to consider in the analysis. It has been found that the temperature gives the possible of modifying the device performances, i.e., the electronic circuits’ parameters operating conditions used in high frequency and high power applications.

Our simulation results are done for submicron GaAs MESFET for different gate lengths and different doping concentrations. This is under the uniform charge distribution in the active region and in the semi-insulating substrate. These characteristics are obtained for the case of ion implementation method. The effects of these parameters on the drain current-voltage characteristics are presented and compared to that published in literature. The results are found to be in good agreement, which validates our simulations.

REFERENCES


[12] DEVSIM TCAD Semiconductor Device Simulator, Available at: https://devsim.org/