AN ACCURATE MODELING OF DELAY AND SLEW METRICS FOR ON-CHIP VLSI RC INTERCONNECTS FOR RAMP INPUTS USING BURR’S DISTRIBUTION FUNCTION

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Abstract
This work presents an accurate and efficient model to compute the delay and slew metric of on-chip interconnect of high speed CMOS circuits for ramp input. Our metric assumption is based on the Burr’s Distribution Function. The Burr’s distribution is used to characterize the normalized homogeneous portion of the step response. We used the PERI (Probability distribution function Extension for Ramp Inputs) technique that extends delay metrics and slew metric for step inputs to the more general and realistic non-step inputs. The accuracy of our models is justified with the results compared with that of SPICE simulations.

Keywords:

1. INTRODUCTION

As the scale of process technology is steadily shrinking towards the ultra deep sub micrometer regime and the size of the design increases to a large extent, length of interconnect is getting longer [1]. So, efficient and accurate computation of delay and slew metric is crucial for enhancing the switching speed of present day devices. The timing verification is a very complicated issue in IC design process because of the statistical variations in the device and interconnects delays. Different proposals have been made to meet the timing constraints. The Elmore delay metric [2] is a popular method for the fast calculation of the interconnect trees. This is because it is simple, closed-form, and easy to evaluate. However with development of the technology, interconnect delay is becoming comparable in value to cell delay or even dominates it, so in this case much more accurate interconnect delay metrics are desired [3]. Many approaches primarily concentrated to find the interconnect delay rather than gate delay so that one can increase the speed of the circuit by simply decreasing interconnect length. Accurate and exact calculation of propagation delay and slew in VLSI interconnects are critical to the design of high speed systems. Current techniques are based on either simulation or analytical models. Slew rate indicates the rate of change of the signal. An increase in slew enhances the delay through the line. Hence an accurate estimation of the slew metric is thus essential for efficient design of high speed CMOS integrated circuits. As the design parameters like gate oxide thickness, channel length reach their threshold, computation of slew metric and interconnect delay become crucial for both performance and physical design optimization for high speed CMOS integrated circuits[3]. AWE [4] can approach towards SPICE-like accuracy by computing and matching higher order moments of the impulse response, but AWE is not a simple closed-form formula and in involves finding a solution of a non-linear equation. So the desired delay and slew metric should be not only highly accurate but also simple and closed-form.

In this paper, a closed form delay and slew metrics are presented based on the Burr’s probability distribution function. It is shown that the moment matching to the Burr’s distribution parameters produces explicit expression for delay and slew metrics. PERI technique [5] is used for extending the delay metric derived for a step input into a delay metric for a ramp input for RC trees and it is valid over all input slew conditions. Note that, the delay metric reduces to the Elmore delay of the circuit under the limiting case of an infinitely slow ramp, a fact first proved in [4] to establish the Elmore delay as an upper bound.

The rest of the paper is organized as follows: Section 2 discusses the basic theory, expressions of circuit moments in terms of impulse response and expressions of mean and variance in terms of circuit moments. Section 3 describes the properties of Burr’s distribution function and discusses the method to calculate the proposed delay and slew matrices. Section 4 shows the experimental results and the comparison with other established matrices. Finally Section 5 concludes the paper.

2. BASIC THEORY

2.1 MOMENTS OF A LINEAR CIRCUIT RESPONSE

Let h (t) is the circuit impulse response in the time domain and let H (s) be the corresponding transfer function. By definition, H(s) is the Laplace transform of h (t)

\[ H(s) = \int_{0}^{\infty} h(t) e^{-st} dt \tag{1} \]

Applying a Taylor series expansion of e^{-st} about s = 0 yields:

\[ H(s) = \int_{0}^{\infty} h(t) \left( 1 - st + \frac{1}{2!} s^2 t^2 - \frac{1}{3!} s^3 t^3 + \ldots \right) dt \]

\[ = \sum_{i=0}^{\infty} \frac{(-1)^i}{(i)!} s^i \int_{0}^{\infty} t^i h(t) dt \tag{2} \]

The \(i^{th}\) circuit-response moment [7], \(m_i\) is defined as:

\[ m_i = \frac{(-1)^i}{(i)!} \int_{0}^{\infty} t^i h(t) dt \tag{3} \]

From (2) and (3), the transfer function H (s) can be expressed as:

\[ H(s) = m_0 + m_1 s + m_2 s^2 + m_3 s^3 + \ldots \tag{4} \]

2.2 CENTRAL MOMENTS

Central moments are distribution theory concepts. Let us consider the moment definition given again:
\[
m_q = \frac{(-1)^q}{(q)!} \int_0^\infty t^q h(t) dt
\]  
(5)

The mean of the impulse response is given by,
\[
\mu = \int_0^\infty h(t) dt
\]  
(6)

It is straightforward to show that the first few central moments can be expressed in terms of circuit moments as follows [6]:
\[
\mu_0 = m_0, \mu_1 = 0, \mu_2 = 2m_2 - \frac{m_4}{m_2}, \mu_3 = -6m_3 + 6 \frac{m_4 m_2}{m_6} - 2 \frac{m_6}{m_4}
\]  
(7)

The central moments have the following geometrical interpretations [6]:
\(\mu_0\) is the area under the curve. It is generally unity, or else a simple scaling factor is applied.
\(\mu_2\) is the variance of the distribution. It measures the spread or the dispersion of the curve from the center. A larger variance results a larger spread of the curve.
\(\mu_3\) is a measure of the skewness of the distribution; for a unimodal function its sign determines if the mode (global maximum) is to the left or to the right of the expected value (mean). Its magnitude is a measure of the distance between the mode and the mean.

### 2.3 SECOND AND THIRD CENTRAL MOMENTS IN RC TREES

The second and third central moments [6] are always positive for RC tree impulse responses. From (8), it is obvious that the second order central moment is positive.
\[
\mu_2 = \int_0^\infty (t - \mu)^2 h(t) dt
\]  
(8)

The impulse response, \(h(t)\), at any node in an RC tree is always positive. Hence, the second central moment, \(\mu_2\) is always positive.

### 2.4 MOMENTS OF PROBABILITY FUNCTION

A probability function is a real valued set function where the domain is a subset of the sample space, \(S\), and the range is a real number in the interval \([0, 1]\). Generally, a function \(Pr\{\cdot\}\) should satisfy the three Kolmogorov axioms [8], or equivalent conditions, in order to be considered as the probabilities function:

i. \(Pr\{S\} = 1\);  
ii. \(Pr\{A\} \geq 0\) for all \(A \subseteq S\);  
iii. \(Pr\{A \cup B\} = Pr\{A\} + Pr\{B\}\) if \(A \cap B = \emptyset\), \(A \subseteq S\), \(B \subseteq S\).

The distribution function of a continuous random variable \(T\) denoted as \(F_T(t)\) provides the value of \(Pr\{T \leq t\}\) for any real number \(-\infty \leq t \leq \infty\). The associated probability density function (PDF) denoted as \(f_T(t)\) is the derivative of the distribution function with respect to \(t\), thus,
\[
f_T(t) = \frac{df_T(t)}{dt}
\]  
(9)

The median, \(t_{0.5}\), is defined by
\[
P\{t \leq t_{0.5}\} = F_T(t_{0.5}) = \int_0^{t_{0.5}} f_T(t) dt = 0.5
\]  
(10)

Whereas, the expected value or mean, \(E[T]\) of a continuous random variable \(T\) with distribution \(f_T(t)\) is
\[
E[T] = \int_{-\infty}^{\infty} t f_T(t) dt
\]  
(11)

The mean is also the first moment of the distribution (or PDF). In general, the \(i^{th}\) moment \(m_i\) of the distribution is,
\[
m_i = E[T^i] = \int_{-\infty}^{\infty} t^i f_T(t) dt
\]  
(12)

### 2.5 RELATION BETWEEN PROBABILITY DENSITY FUNCTIONS AND CIRCUIT RESPONSES

Any function \(f(t)\) can be treated as a probability density function [6] if it is defined in the range \([a, b]\) and satisfies
\[
\begin{align*}
\int_a^b f(t) dt &= 1 \\
f(t) &\geq 0 \quad \forall t
\end{align*}
\]  
(13)

If \(f(t)\) is equal to zero outside of the range \([a, b]\), we can replace the integration limits in Eq. (13) with \(-\infty \rightarrow \infty\). Elmore was the first to apply moments for delay approximation of a limited class of circuit responses by observing that the impulse response of a circuit can be treated as a probability density function. Elmore used this observation to justify the approximation of the 50% point of a monotonic step response (the median point of the impulse response) by the first moment (mean of the impulse response). It was shown that the impulse response corresponding to an RC tree is unimodal with positive skew [6]. From this it follows that the mode is less than the median which is less than the mean and vice versa [7-8]: (Skew > 0) if and only if (mode < median < mean). This proved that the Elmore delay is an upper bound for the 50% step response delay, and was shown to hold for finite input signal rise time. An important observation [6] is that because of the variation in impulse response shapes along an interconnect path, the relative accuracy of the Elmore delay bound can be quite poor. Especially for interconnects associated with deep submicron technologies, more than one moment is needed to capture the waveform shape-characteristics.

### 3. BURR DISTRIBUTION MODEL

Elmore delay model believes the similarity between non-negative impulse responses and probability density functions (PDF). In theory, Elmore’s assumption can be easily extended beyond simply estimating the median by the mean, if one considers higher order moments to characterize the distribution function. Once characterized, the delay can be approximated via table-lookup of the median value for the representative distribution family. In this work, a novel delay and slew metrics are proposed using Burr probability distribution. The Burr’s distribution is a two parameter continuous distribution [9]. Since both are unimodel and have non-negative skewness [10], one can match the impulse response of the generalized RC network to the characterize parameters of the Burr distribution. The PDF of Burr’s distribution [9] is shown in Fig. 1. The probability density function of the burr’s distribution \(f_{ck}(x)\), is a function of one variable \(x\) and two parameters \(c\) and \(k\) (positive real numbers)[9].
Where $E(x^2) = \frac{k c^2}{\Gamma(k+1)}$.

Mean for the burr distribution is given as,

$$Mean = E(x) = kB \left( k - \frac{1}{c}, 1 + \frac{1}{c} \right)$$

(15)

Where $B(x, y)$ is the incomplete beta function.

The relation between beta and gamma function is given as,

$$\Gamma(x) = \frac{\Gamma(x+y)}{\Gamma(x)\Gamma(y)}$$

(19)

By using the gamma function approximation [11], we get,

$$\Gamma(z + b) = \sqrt{2\pi e^{-z-z}} (z + b)^{-\frac{1}{2}}$$

(20)

By equation (19), we get the Mean as,

$$E(x) = k \left( k - \frac{1}{c} \right) \frac{1}{\Gamma(k+1)}$$

(21)

The expression presented above is the 50% delay metric for step input.

Substituting the values of $c$ and $k$ from (32), (33) in (34), we have,

$$median = \left[ \frac{2}{\pi} \right] \left[ \frac{0.9196}{m_1} \right]$$

(35)

So, we can write the closed form delay expression as,

$$Delay(50\%) = \left[ 2 \left( \frac{0.9196}{m_1} \right) \left( \frac{2.4709}{m_1 m_2} \right) \right]^{-1}$$

(36)

The expression presented above is the 50% delay metric for step input for generalized RC network. From (36) we can see that the median i.e. 50% delay metric is the simple function of the first two circuit moments. This is our proposed closed form model using burr’s distribution.

Let us assume that the input waveform is a ramp with slope $T$, as shown in Fig. 2(a) [12]. The PDF of this waveform is a uniform distribution with mean $\mu(T) = T/2$ and standard deviation $\sigma(T) = T/4$. Thus, the delay of the output ramp is as shown in Fig. 2(b).
Fig. 2. Ramp input and its corresponding response of an RC network

If \( \mu(s) = -m_j \) is the Elmore delay and \( M(S) \) is the step delay metric as given by equation (36). The delay estimation for the ramp response [5] is given by,

\[
D(R) = (1-\alpha) \mu(s) + \alpha M(S)
\]

(37)

Where \( \alpha \) is a constant and is given by,

\[
\alpha = \left( \frac{2m_j-m^2}{2m_j-m^2+\frac{T^2}{12}} \right)^{\frac{1}{2}}
\]

(38)

Where 0 \( \leq T < \infty \) is the slope of the ramp input as shown in Fig. 2(a). From (36), (37) and (38), we get

\[
d(k) \left\{ \frac{2m_j-m^2}{2m_j-m^2+\frac{T^2}{12}} \right\} \left[ \frac{2}{2m_j-m^2+\frac{T^2}{12}} \right] = \left[ \frac{2.4709}{m^2 T^2} \right]^{\frac{1}{2}}
\]

\[
= \left( \frac{2.4709}{m^2 T^2} \right)^{\frac{1}{2}}
\]

(39)

The above derived equation (39) is the delay metric equation for the Burr’s Distribution function for ramp input. This is our proposed closed form delay model for ramp input for on-chip VLSI interconnect using burr probability distribution.

3.2 PROPOSED SLEW MODEL

Burr’s cumulative distribution function [9], as a function of \( t \), is given by,

\[
F(t) = 1 - \frac{1}{(1 + t')^\frac{1}{2}} \quad t \geq 0
\]

(40)

If \( F(t) \) satisfies the following conditions: they are

\[
0 \leq F(t) \leq 1 \quad \text{and} \quad \lim_{t \to 0} F(t) = 1 \quad \lim_{t \to \infty} F(t) = 0
\]

After applying Binomial approximations [7],

\[
t = \left[ \frac{1}{F(t)} \right]^{\frac{1}{2}}
\]

(41)

Now, let \( T_{LO} \) and \( T_{HI} \) be 10% and 90% delay points, respectively. Matching these points to the CDF yields from equation (41),

\[
T_{LO} = \left[ \frac{0.1}{k} \right]^{\frac{1}{2}}
\]

(42)

And

\[
T_{HI} = \left[ \frac{0.9}{k} \right]^{\frac{1}{2}}
\]

(43)

The Burr’s slew metric is calculated by using equations (42) and (43) as,

\[
BSM = T_{HI} - T_{LO} = \left[ \frac{0.9}{k} \right]^{\frac{1}{2}} - \left[ \frac{0.1}{k} \right]^{\frac{1}{2}}
\]

(44)

By substituting the values of the \( c \) and \( k \) from (32) and (33) in (44), we get,

\[
BSM = \frac{0.9196}{m} \left( \frac{0.9}{\ln 2.5m_m^2} - \frac{0.1}{\ln 2.5m_m^2} \right)
\]

(45)

This is the proposed closed form model for the slew metric for step input for on-chip VLSI interconnects based on the burr’s distribution. From the above equation it can be seen that Burr Slew Metric [BSM] is the mere function of the first two circuit moments.

The output slew is the root-mean square of the step slew and input slew [5]. For ramp slew,

\[
Slew(R) = \sqrt{Slew^2(S) + Slew^2(I)}
\]

(46)

Further, (46) exhibits the right limiting behavior: As \( Slew(I) \to \infty \) we get \( Slew(R) \to \infty \) and as \( Slew(I) \to 0 \), we have \( Slew(R) \to Slew(S) \).

Where Slew(S) is the step slew metric which is given by equation (45) and Slew (I) is the input slew which is given as,

\[
Slew^2(I) = \frac{T^2}{12}
\]

(47)

From equations (45), (46) and (47), we get,

\[
Slew(R) = \sqrt{\left( \frac{0.9196}{m} \left( \frac{0.9}{\ln 2.5m_m^2} - \frac{0.1}{\ln 2.5m_m^2} \right) \right)^2 + \frac{T^2}{12}}
\]

(48)

The above derived equation (48) is the slew metric equation for the Burr’s Distribution function for ramp input. This is our proposed closed form model using burr’s distribution.

4. EXPERIMENTAL RESULTS

We have implemented the proposed delay and slew estimation method using burr’s distribution and applied it to widely use actual interconnect RC networks as shown in Fig. 3. For each RC network source we put a driver, where the driver is a voltage source followed by a resistor.

Fig. 3. An RC Tree Example

In order to verify the efficiency of our model, we have extracted 208 routed nets containing 2024 sinks from an
industrial ASIC design in 0.18 \mu m technology. We choose the
nets so that the maximum sink delay is at least 10 ps and the
delay ratio between closest and furthest sinks in the net is less
than 0.2. It ensures that each net has at least one near end sink.
We classify the 2244 sinks as it was taken in PERI [5] into the
following three categories:
• 1187 far-end sinks have delay greater or equal to 75% of
  the maximum delay to the furthest sink in the net.
• 670 mid-end sinks which have delay between 25% and 75% of
  the maximum delay and,
• 367 near-end sinks which have delay less than or equal to 25%
  of the maximum delay.
In order to find the delay and slew at node 5 for ramp input, a
saturate ramp of time period of T=100 ps is used. For both
calculation of delay and slew, the relative error is less than 2%.
The calculated average, minimum, maximum values are
compared along with standard deviation for the delay calculated
by using PERI and with those found using the proposed model.
The comparative results are summarized in Table 1.

In the final step of the experiments, we have estimated the
effect of input slew on the delay and slew estimation for the same
seven node RC network as shown in the Fig. 3. We have chosen
10/90 input slew values of 250 and 500 picoseconds, respectively
for ramp input. We have used burr distribution to find the
delay ratio between closet and furthest sinks in the net.

The obtained average, minimum, maximum values are
compared along with standard deviation for slew calculated by
using PERI method and with those found using our proposed
model. The comparative results are summarized in Table 2.

In the final step of the experiments, we have estimated the
effect of input slew on the delay and slew estimation for the same
seven node RC network as shown in the Fig. 3. We have chosen
10/90 input slew values of 250 and 500 picoseconds, respectively
and delays and slaws were estimated for each node. The
comparative result of our proposed model with PERI [5] and
RICE [13] are shown in the Table 3 and Table 4 for delay and
slew, respectively.

### Table 1 Comparison between Proposed Delay Model and PERI

<table>
<thead>
<tr>
<th>Step Delay</th>
<th>Delay Metric Using Two Moments</th>
</tr>
</thead>
<tbody>
<tr>
<td>For Ramp</td>
<td>PERI Method</td>
</tr>
<tr>
<td>Sinks</td>
<td>Avg</td>
</tr>
<tr>
<td>Near</td>
<td>1.25</td>
</tr>
<tr>
<td>Mid</td>
<td>1.14</td>
</tr>
<tr>
<td>Far</td>
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<tr>
<td>Total</td>
<td>1.08</td>
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### Table 2 Comparison between Proposed Slew Model and PERI

<table>
<thead>
<tr>
<th>Step Slew</th>
<th>Slew Metric Using Two Moments</th>
</tr>
</thead>
<tbody>
<tr>
<td>For Ramp</td>
<td>PERI Method</td>
</tr>
<tr>
<td>Sinks</td>
<td>Avg</td>
</tr>
<tr>
<td>Near</td>
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</tr>
<tr>
<td>Mid</td>
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<tr>
<td>Far</td>
<td>1.13</td>
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<tr>
<td>Total</td>
<td>1.04</td>
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### Table 3 Delay Comparison for Each Node between Proposed Delay Model, PERI and RICE

<table>
<thead>
<tr>
<th>Input Slew</th>
<th>250(ps)</th>
<th>500(ps)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Node</td>
<td>PERI</td>
<td>RICE</td>
</tr>
<tr>
<td>1</td>
<td>207</td>
<td>210</td>
</tr>
<tr>
<td>2</td>
<td>383</td>
<td>383</td>
</tr>
<tr>
<td>3</td>
<td>484</td>
<td>482</td>
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<tr>
<td>4</td>
<td>707</td>
<td>705</td>
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<td>6</td>
<td>461</td>
<td>461</td>
</tr>
<tr>
<td>7</td>
<td>925</td>
<td>923</td>
</tr>
</tbody>
</table>

### Table 4 Slew Comparison for Each Node between Proposed Slew Model, PERI and RICE

<table>
<thead>
<tr>
<th>Input Slew</th>
<th>250(ps)</th>
<th>500(ps)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Node</td>
<td>PERI</td>
<td>RICE</td>
</tr>
<tr>
<td>1</td>
<td>1614</td>
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<tr>
<td>2</td>
<td>1725</td>
<td>1733</td>
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<td>2003</td>
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<td>7</td>
<td>2237</td>
<td>2233</td>
</tr>
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</table>

### 5. CONCLUSION

In this paper, we have proposed an efficient and accurate
interconnect delay and slew metric for high speed VLSI designs
for ramp input. We have used burr distribution to find the
desired matrices. It is found that the proposed matrices are a
simple function of first two moments. Our model has Elmore
delay as upper bound but with significantly less error and does
not require any complex look up table. The novelty of our
approach is justified by the calculated delay from the
experiments performed on the industrial nets.

### REFERENCES

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Approach for Delay Evaluation for On-Chip RC


